IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): TOTANI et al.

Atty. Dkt.: 01-598

Serial No.: Unknown

Group Art Unit:

Filed: Concurrently herewith

Examiner:

Title: EQUIPMENT AND METHOD FOR

BONDING PRINTED CIRCUIT

BOARDS

Date: April 22, 2004

Commissioner for Patents Arlington, VA 22202

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the reference(s) listed on the attached Form PTO-1449 is/are submitted for consideration by the Examiner without any admission that it/they constitute(s) statutory prior art, or without any admission that it/they contain(s) subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed reference(s) has/have been considered and made of record.

Respectfully submitted,

David G. Posz Reg. No. 37,701

Posz & Bethards, PLC 11250 Roger Bacon Drive, Suite 10 Reston, VA 20190 (703)707-9110 (phone) Customer No. 23400

* PATENT APPLICATION

Page 1 of 1

FORM PTO-1449	ATTY. DKT NO.	T 01-598 SER. NO.		
	APPLICANT	TOTANI et al.		
	FILING DATE	April 22, 2004	GROUP	

REFERENCE DESIGNATION

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE NAME		CLASS	SUB CLASS
	US 2003/0079341 A1	May 1, 2003	Miyake et al.		
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FOREIGN PATENT DOCUMENTS

TRANSLATION

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DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS	YES	N	NO Eng.
JP-A-10-270820 (Discussed on page 2 of the spec.)	ne 10/9/98	JAPAN						Abstract X

^{*} Full English text is available in machine-translated form in JPO (Japanese Patent Office) English language web site at http://www1.ipdl.jpo.go.jp/PA1/cgi-bin/PA1INDEX.

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

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Rev. 10/94 (Form 3.05)